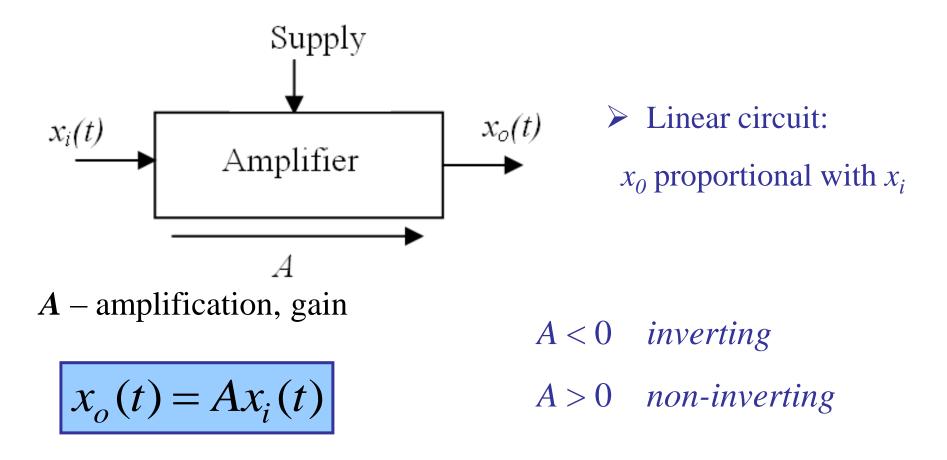
# **Electronic Amplifiers**

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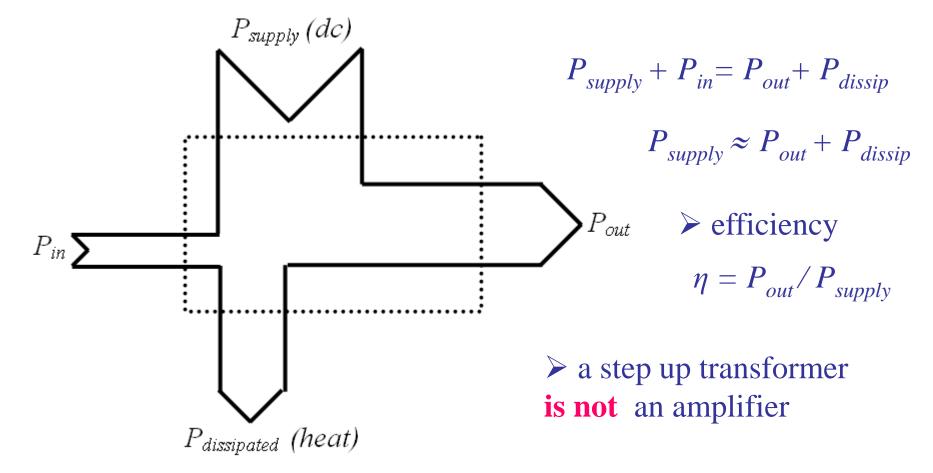
**Amplifier: activ three-port network** that delivers to the output a signal  $x_o(t)$  (voltage or current) with **the same shape** as the input signal  $x_i(t)$  and can provide **greater power** on an adequated load.



#### **Power transfer and power balance**

→ the average power of the output signal  $P_{out}$  is greater than the average power of the input signal  $P_{in}$ .

> the excess of the output power is taken from the supply sources



### **Amplifier models**

two-port network: it consider explicitly only the behavior to the input and output ports and input-output transfer for the signal
valid, irrespective of the internal complexity of the amplifiers

valid in the bandpass frequency domain

#### Linear controlled sources

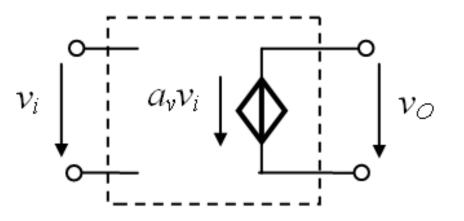
➤ active two-port network – only one finite, non-zero parameter: forward transfer parameter (gain)

➤ the output signal (voltage -  $v_o$ ) is controlled by the input signal (voltage -  $v_i$ )

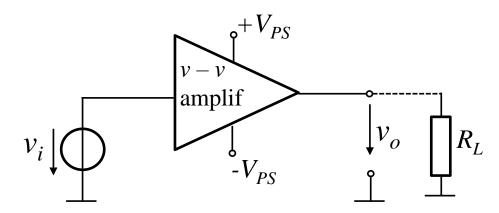
➢ pseudo-sources

**Example: VCVS** 

 $v_0 = a_v v_i$ 



#### Noninverting amplifier, symmetric differential supply



$$A_v > 0$$
$$v_o = A_v v_i$$

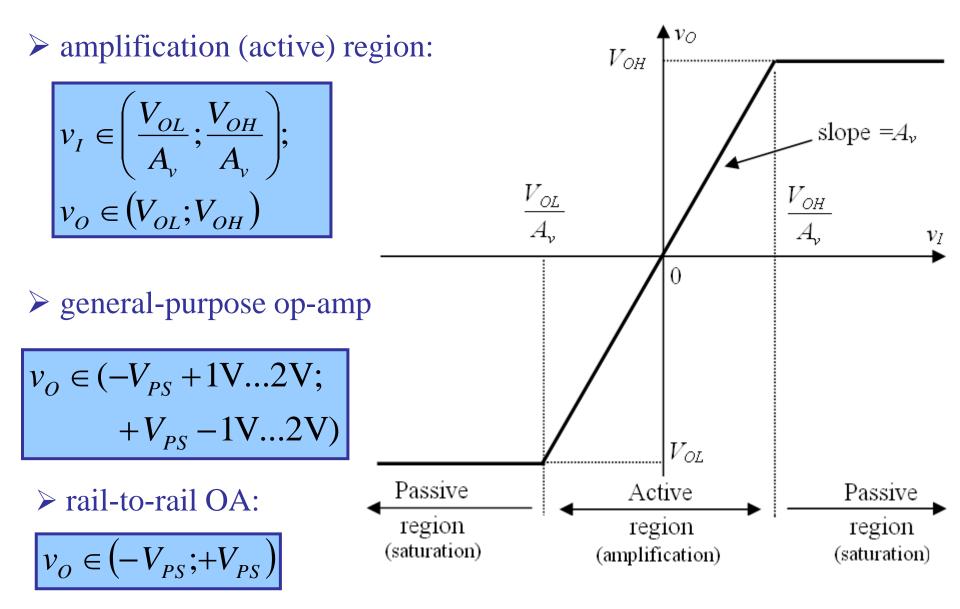
 $\succ$  general-purpose op-amp

 $v_0 \in (-V_{PS} + 1V...2V; +V_{PS} - 1V...2V)$ 

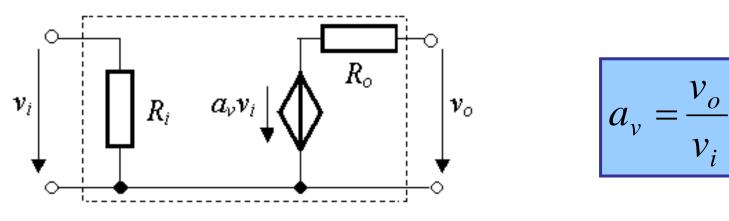
➤ rail-to-rail op amp:

 $v_0 \in (-V_{PS}; +V_{PS})$ 

# VTC for the voltage-to-voltage, noninverting amplifier, symmetric differential supply



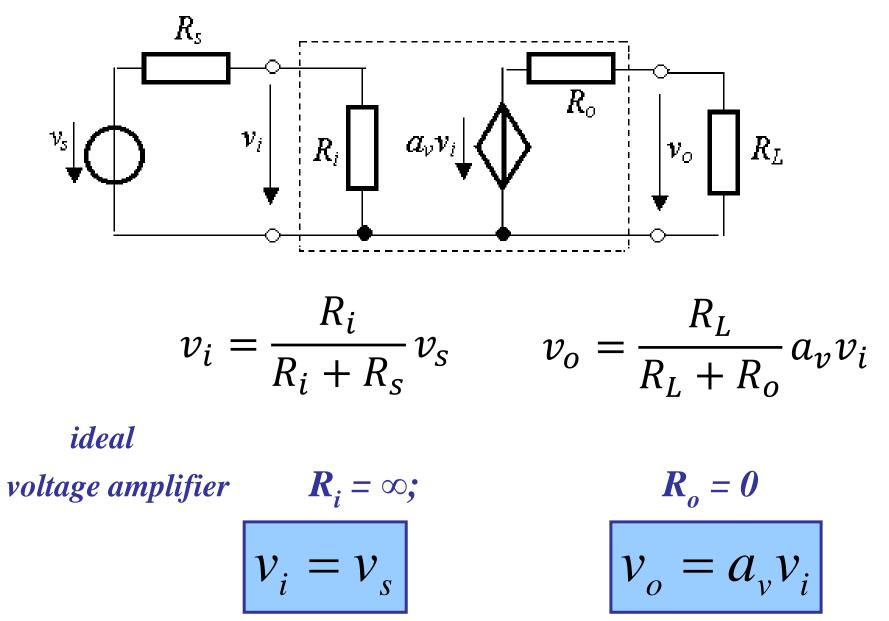
# **Modeling the voltage amplifier**



 $R_i$  – draws current from the input signal source

 $R_o$  – deteriorates the output voltage in the presence of load (voltage divider)

#### Amplifier connected with a signal source and a load resistance



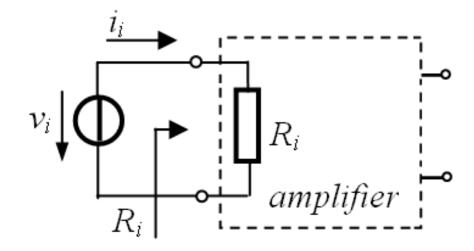
## **Determining the amplifier performances**

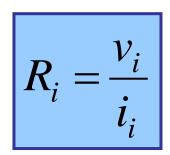
- gain (forward transfer factor)
- ➢ input resistance
- output resistance

## Gain

- analysis of the circuit using theorems and electrical circuit relations (Kirchhoff, Ohm, etc.) and equations describing the operation of the active devices
- express the output signal as a function of the input signal and compute the gain

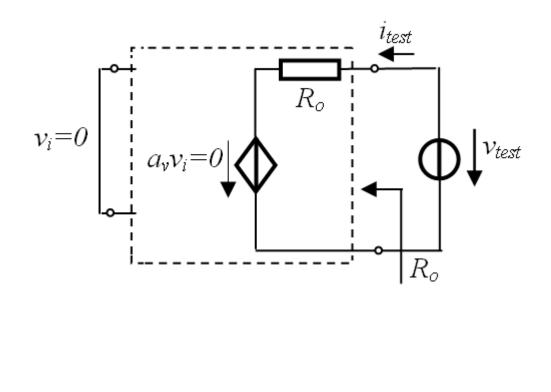
#### **Input resistance**





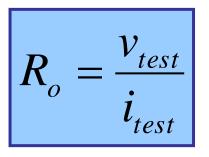
# > The resistance seen by the signal source when it looks to the circuit

#### **Output resistance**



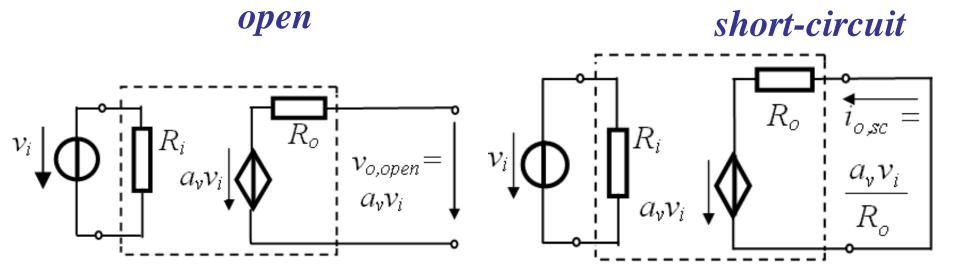
Set the input signal source to zero

Connect a test source to the output



> The resistance seen by the load when it looks back to the circuit and input signal source is set to zero

#### **Output resistance – alternative method**



Optiona

$$R_o = \frac{v_{o,open}}{i_{o,sc}}$$